

What is claimed is:

1. A method for remapping locations in memory, comprising:
generating a remapping value;
logically combining the remapping value with an intended address value to generate a remapped address value; and
accessing a memory location having the remapped address value.
2. The method of claim 1, wherein the remapping value is generated by logically combining a bad address value with an unused address value.
3. The method of claim 2, wherein the bad memory address value, unused memory address value, and the remapped address value correspond to individual data locations.
4. The method of claim 2, wherein the bad memory address value and the unused memory address value each correspond to multiple data locations.
5. The method of claim 2, wherein the bad memory address value, unused memory address value, and the remapped address value correspond to individual memory pages.
6. The method of claim 2, wherein the bad memory address value and the unused memory address value each correspond to multiple memory pages.
7. The method of claim 1, wherein the remapping value is logically combined with each intended address value to remap each intended address to individual remapped address values.
8. The method of claim 1, wherein one or more bad memory address values exist, and wherein the remapping value is logically combined with only intended address values that equal one of the bad memory address values to generate a remapped address value for only the one or more bad memory address values.

9. The method of claim 2, wherein the bad memory address value and the unused memory address value are exclusively-ORed to produce the remapping value.
10. The method of claim 1, wherein the remapping value and the intended address value are exclusively-ORed to produce the remapped address value.
11. The method of claim 2, wherein the bad memory address value and the unused memory address value are exclusively-NORed to produce the remapping value.
12. The method of claim 1, wherein the remapping value and the intended address value are exclusively-NORed to produce the remapped address value.
13. The method of claim 1, wherein the remapping value is latched.
14. A system for remapping locations in memory, comprising:
 - a first logic for outputting a remapping value;
 - a second logic configured to combine the remapping value with an intended address value to generate a remapped address value; and
 - a memory address input configured to access a memory location having the remapped address value.
15. The system of claim 14, wherein the first logic generates the remapping value by combining a bad memory address value with an unused memory address value.
16. The system of claim 15, wherein the bad memory address value, unused memory address value, and the remapped address value correspond to individual data locations.
17. The system of claim 15, wherein the bad memory address value and the unused memory address value each correspond to multiple data locations.

18. The system of claim 15, wherein the bad memory address value, unused memory address value, and the remapped address value correspond to individual memory pages.
19. The system of claim 15, wherein the bad memory address value and the unused memory address value each correspond to multiple memory pages.
20. The system of claim 14, wherein the second logic combines the remapping value with each intended address value to remap each intended address to individual remapped address values.
21. The system of claim 15, wherein the second logic combines the remapping value with only the intended address value that equals the bad memory address value to generate a remapped address value for only the bad memory address value.
22. The system of claim 15, wherein the first logic is an exclusive OR gate.
23. The system of claim 14, wherein the second logic is an exclusive OR gate.
24. The system of claim 15, wherein the first logic is an exclusive NOR gate.
25. The system of claim 14, wherein the second logic is an exclusive NOR gate.
26. The system of claim 14, further comprising a latch configured to store the remapping value.